2122 TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT Docket No. (Under 37 CFR 1.97(b) or 14292 In Re Application Of: Shinya Furusawa JUN 1 6 2004 Serial No. Filing Date Examiner **Group Art Unit** 09/777,543 Feb. 6, 2001 Unassigned 2122 Title: SYSTEM AND METHOD FOR VERIFYING HARDWARE DESCRIPTION RECEIVED JUN 1 8 2004 Address to: Technology Center 2100 **Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450 37 CFR 1.97(b) The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114. 37 CFR 1.97(c) 2. The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of: ☐ the statement specified in 37 CFR 1.97(e); OR ☐ the fee set forth in 37 CFR 1.17(p).

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) 1.97(c))				Docket No. 14292	
In Re Application: Shinya Furusawa JUN 1 6 2004					
Serial No.	Filing Date	PADEMARK	Examiner		Group Art Unit
09/777,543	Feb. 6, 2001		Unassigned		2122
SYSTEM AND METHOD FOR VERIFYING HARDWARE DESCRIPTION					RECEIVED JUN 1 8 2004
					Technology Center 2190
Payment of Fee					
(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))					
The Director is hereby authorized to charge and credit Deposit Account as described below. Charge the amount of Credit any overpayment. Charge any additional fee required. Certificate of Transmission by Facsimile* Certificate of Mailing by First Class Mail Certify that this document and authorization to charge deposit account is being facsimile transmitted to the United States Patent Certify that this document and fee is being deposited on June 14, 2004 vith the U.S. Postal Service as					
and Trademark Office (Fo	2X. NO.) (on	first class mail under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.		
Signature			Signature of Person Mailing Correspondence		
			Paul J. Esatto, Jr.		
Typed or Printed Name of Person Signing Certificate			Typed or Printed Name of Person Mailing Certificate		
*This certificate may only be used if paying by deposit account. Dated: June 14, 2004 Signature Paul J. Esatto, Jr., Reg. No. 30,749					
SCULLY, SCOTT, MUPRHY & PRESSER					
400 Garden City Plaza					
Garden City, NY 11530 (516) 742-4343					
PJE:ahs					

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE plicant: Shinya FURUSAWA **Examiner:**

Serial No.:

Unassigned

09/777,543

Group Art Unit:

2122

Filed:

Feb. 6, 2001

Docket:

14292

For:

SYSTEM AND METHOD FOR VERIFYING

HARDWARE DESCRIPTION

Dated:

June 14, 2004

Confirmation No.

7216

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Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 23313-1450

JUN 1 8 2004

Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Sir:

In accordance with 37 C.F.R §§1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

- 1. Japanese Laid-Open Patent Application No. 10-116302, dated May 6, 1998;
- 2. Japanese Laid-Open Patent Application No. 3-6737, dated January 14, 1991; and

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P. O. Box 1450, Alexandria, VA 22313-1450 on June 14, 2004.

Dated: June 14, 2004

Paul J. Esatto, Jr.

3. T. Miyake, "Up-to-the-Minute Hardware Designs Using C Language", Vol. 758, pp. 177-191, Nikkei Electronics, Japan Nikkei BP, (Nov. 29, 1999).

The references were cited in an Office Action dated March 24, 2004 received from the Japanese Patent Office. Applicant is submitting copies of the above-cited references, together with an English translation of the Examiner's comments regarding the references from the Official Action. The relevance of the references is described in the Official Action.

In compliance with the requirements of 37 C.F.R. §1.98(a)(3), as a concise statement of relevance, as it is presently understood by the individual designated in 37 C.F.R. §1.56(c) most knowledgeable about the content of the information, the undersigned attorney of record submits a translation of portions of an official action by a foreign examiner in which the references were cited. The relevance to the pending U.S. patent application is that the references were cited in a foreign patent application on the same subject matter. However, no independent analysis of the references, the accuracy of the statement of the foreign examiner or the claims of the foreign application under the laws of that country or the United States relative to the subject matter claimed in the present application has been made; the present understanding of the contents thereof by the undersigned being based on the translation of the foreign examiner's comments submitted herewith.

The undersigned attorney hereby states that each item of information contained in this Information Disclosure Statement was first cited in any communication from a foreign patent

office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

Respectfully submitted,

Paul J. Esatto, Jr. Reg. No.30, 749

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